

The natural forward voltage drop of D1 and D2 furnishes self-bias, and bias conditions for D3 are satisfied by a fixed resistor R1. The values of slope adjusters VR1, VR2, VR3, VR4, VR6, VR8 and VR10 were selected to give a parabolic function approximating to $E_{\nu} = E_{\mu i}^*$ when all sliders are at mid-track, and appropriate bias values for that function are provided by mid-track settings of breakpoint adjusters VR5, VR7, VR9 and VR11. The combination VR12 and R3 serves to eliminate offset voltages resulting from diode leakage currents, and VR12 is therefore used for zero-setting.

With so many possible adjustments, including amplifier closed-loop gains determined by R or $R_{\rm H}$ computing resistors, it is obviously impossible to catalogue the coverage of the Fig. 8.1 circuit. As a rough indication though, powers of $E_{\rm In}$ ranging from about $E_{\rm In}^{-1}$ to beyond $E_{\rm In}^{-2}$ to elyone $E_{\rm In}^{-2}$ to help of the first operational amplifiers, the upper limit will extend beyond $E_{\rm In}^{-2}$. Corresponding root functions $^{1,1}\sqrt{E_{\rm In}}$ to $^{4}\sqrt{E_{\rm In}}$ may also be generated. It is sometimes possible to use the UNIT "C" function generator for certain trigonometrical functions, and logs to the base 10 or e.

UNIT "C" BOX

A wood and plastics laminate box, of small dimensions compared with other PEAC units, will serve to house the two function generator circuit panels. The suggested form of construction is shown in Fig. 8.2. Softwood blocks are glued to a $9 \frac{1}{11} \times 4 \frac{1}{11} \times \frac{1}{11} \times$

ANALOGUE

COMPUTER

By D.BOLLEN

Last month the Function Generator UNIT "C" was introduced. The principle of operation and some of the uses of the function generator were explained. We are continuing with a description of the practical circuit, constructional details, and application information.

FUNCTION GENERATOR CIRCUIT

The function generator circuit of Fig. 8.1 is designed to display a nominal resistance of 100 kilohm when the input voltage is ±1V. A typical resistance variation with applied voltage is from 500 kilohms at 0.2V to 10 kilohms at 10V. In the Fig. 8.1 circuit, components forming the positive branch are identified by the letter A after a component number, and the letter B is appended to negative branch numbering. As both branches are identical, except for diode and bias polarities, it is not necessary to describe them separately.

DI is a gold-bonded diode, for a low voltage drop with small input voltages. All other diodes (D2-D7) are of silicon construction to keep reverse leakage low.

UNIT "C" FRONT PANEL

The only items to be mounted on the 9½n × 4in plastics laminate front panel are eight coloured sockets; the layout is given in Fig. 83. A series of ½in holes are drilled in the front panel to allow screwdriver access to slope, breakpoint, and set-zero controls. Panel markings are similar to previous PEAC units.

FUNCTION GENERATOR CONSTRUCTION

Two 3½in × 3½in s.r.b.p. panels are drilled and shaped according to the Fig. 8.4a diagram. Before inserting turret tags, lay the prepared panels out as shown in Fig. 8.5, so that one panel is turned over in relation to the other, and components are clearly seen to be mounted on opposite sides. The underside wiring of the positive branch panel is shown in Fig. 8.4c. 8.4b, and the writing of the result in the property of the positive branch is fire 8.4c. 8.4b. and the writing of the results of the positive branch is fire 8.4c. 8.4b. and the writing of the results of the property of the

All diodes are mounted on turret tags to allow them to be disconnected for special purposes, where for

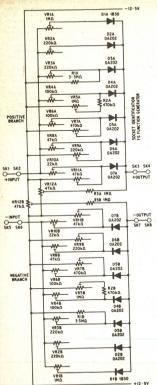


Fig. 8.1. UNIT "C" function generator circuit diagram



Function generator circuit panel

example it is desired to reduce the number of breakpoints, or combine a curved and straight line function. It is advisable to check the polarity of all diodes with a meter before mounting them on the circuit panels.

After completing the underside wiring, bolt the two circuit panels on the plywood frame, as in Fig. 8.5, and make sure that the front panel holes are aligned with the pre-set miniature potentiometer slots.

SETTING UP THE FUNCTION

Patching leads for the function generator should preferably be terminated at one end by miniatint connection to the UNIT "A" computing component sockets. As the generation of powers and roots is the main area of interest, functions related to the square or cube of a number are used in the following setting-up instructions.

To patch the function generator to OAI, join FG/SK5 to S1/II/ISK3, FG/SK8 to S1/II/SK4, S1/SK5 to OAI/SK9, and link together OAI/SK9, SK10, and SK4. Insert a 100 kilohm computing resistor into OAI/SK1 and SK12. Take a patching lead from S1/II/SK1 to VS1/SK2, and ensure that S6 is off. The task of setting un the function generator is made

The task of setting up ne tunktuby geletaston's easier if two voltmeters are used, one for $E_{\rm in} \approx 1000\,{\rm Mes}$ consider to S1/I/15K2, and the other for $E_{\rm in} \approx 1000\,{\rm Al}/{\rm SK}$ 13. The Unit "B" readout meter is ideal for monitoring $E_{\rm in} \approx 1000\,{\rm K}$ 25 which on the computer power supply and zero OA1 by means of its balance control VR15. Set all function generator slope and breakpoint potentionmeter sliders to mid-track, and connect the red and blue wires from the function generator to the power supply terminals on the side of the UNIT "A" box (TL1 and TL2). Adjust VR12 fecro-set) for zero output from OA1.

Because of the interdependence of slope and breakpoint adjustments, a spementic approach is called for
when setting up function. Start with the lowest
performed to the proceed in an orderly fashion towards
to the start of the start of the start of the start
than the maximum E₁, value. It is a help to
tabulate specific input and output voltages and relate
them to particular slope or breakpoint controls.
To assist the reader, two tables have been prepared
covering square and cube functions, Table 8.1 and
Table 8.2.

If a square function is to be set up on the function generator, switch on S6 (Voltage Source) and set VSI orran on cput of -0.2V, then adjust VR1B for an OA1 couptu of 0.4V. Next set VSI for -0.5V and adjust VR2B for an output of 0.4V. Next set VSI for -0.5V and adjust VR2B for an output of 0.5V, and so on, according to Table 8.1. After application of $E_{\rm in} = -2.0V$, and adjustment of VR4, change the 100 kilohm computing resistor in the feedback loop of OA1 to 10 kilohm, to prevent the amplifier overloading when $E_{\rm in}$ exceeds V10.

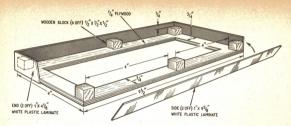


Fig. 8.2, Details and measurements of UNIT "C" function generator case

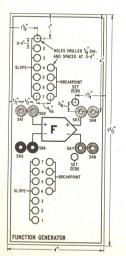


Fig. 8.3. Front panel layout of UNIT "C" function generator

TABLE S.I

Diode	Ein	Adjust	Adjust break- point	Eo	
1 2 3 4	-0.2V -0.5V -1.0V {-1.5V -2.0V	VRI VR2 VR3 VR4	VR5	+ 0-04V + 0-25V + 1-0V + 2-25V + 4-0V	$E_0 = E_{in}^s$ $R_f = 100k\Omega$
5 6 7	$\begin{cases} -2.5V \\ -3.5V \\ -4.0V \\ -6.0V \\ -6.5V \\ -9.0V \end{cases}$	VR6 VR8 VR10	VR7 VR9 VRII	+ 0-625V + 1-225V + 1-6V + 3-6V + 4-225V + 8-1V	$\begin{split} E_0 &= \frac{E_{\rm in}^2}{10} \\ R_f &= 10 k \Omega \end{split}$

COMPONENTS ...

UNIT "C" BOX
Plywood 9¼in × 4in × ¼in
Softwood ¼in × ½in × 3½in
White plastics laminate 9¾in × 1in (2 off), 4kin × lin (2 off) Rubber grommet Lin X 35in

UNIT "C" Front Panel

White plastics laminate 94in × 4in. Sockets: 2 red. 2 yellow, 2 black, 2 blue.

UNIT "C" Function Generator Components Resistors

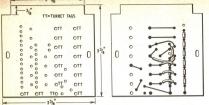
esistors R1 3-3M Ω (2 off) R2 470k Ω (2 off) R3 IM Ω (2 off) R4 IM Ω (2 off) All 10%, $\frac{1}{2}$ W carbon composition

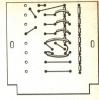
Pre-set Potentiometers VRI, VR5 VR2, VR3, VR9 ΙΜΩ (4 off) 220kO (6 off) VR4. VR6 100kΩ (4 off) VR7 470kΩ (2 off) VR8 47kΩ (2 off VRIO 22kΩ (2 off) VRII, VRI2 47kΩ (4 off)

All miniature horizontal mounting Diodes

DI IB30 (2 off) (Radiospares) D2-D7 OA202 (12 off)

Miscellaneous S.R.B.P. 33in × 33in (2 off). Small turret tags 4mm stackable plugs, one red, one blue (Radiospares)



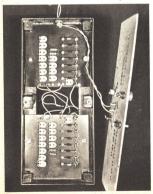


(a) drilling template (2 off)

(b) positive branch underside wiring

(c) negative branch underside wiring

TABLE 8.2									
Diode	Ein	Adjust slope	Adjust break- point		Eo				
1	-0·3V	VRI	-	+	0-027V	1			
2	-0.5V	VR2		+	0·125V	1			
3	-0.75V	VR3	-	+	0-421V	- 1	$E_0 = E_{in}^a$ $R_f = 100k\Omega$		
4	(-1.0V	-	VR5	+		>			
4	1 -1-25V	VR4	-	+	1-953V	1			
5	-1.5V	and the same of	VR7	+	3·375V	4			
3	\(\(\)-2.0\(\)	VR6	-	+	8-0V	J			
	(-2·5V	_	VR9	+	1-56V	٦.	E. 3		
6	1 -3-0V	VR8		4		-	$E_0 = \frac{E_{in}^3}{10}$		
7	(-3.5V	_	VRII	+	4-287V	- ($R_r = 10k\Omega$		
,	1-4-64V	VRIO	_	+	10-0V	J	Mr - TOKES		



Interior view of UNIT "C" function generator

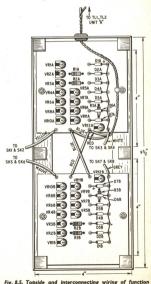


Fig. 8.4. Function generator circuit panels (2 off)

Fig. 8.5. Topside and interconnecting wiring of function generator panels. The circuit boards are shown in position inside the UNIT "C" case

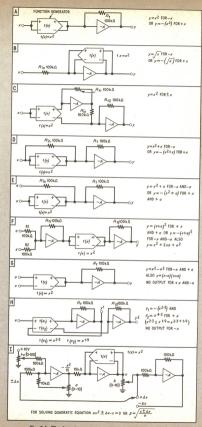


Fig. 8.6. The function generator used for equation solving



This photograph shows PEAC being used to solve simultaneous equations

After the entire range of input voltages listed in Table 8.1 has been covered, return to Ein=0.02V and go through the procedure again, to achieve optimum accuracy. The positive branch can be set up for the same function as the negative branch that the properties of the process of the proces

THE FUNCTION GENERATOR IN EQUATION SOLVING

The fact that an analogue computer can produce and handle imaginary numbers will be particularly evident when the function generator is applied to equation solving, see Fig. 8.6. One type of function generator circuit configuration will produce consistant outputs for, say, the cube of a number, but not for its square, or vice versa, because $\pm x^2 = +y$, but $\pm x^2 = +y$, and $\pm x^2 = +y$. The computer operator must therefore choose, or devise, the appropriate circuit for a given task.

Output y in Fig. 8.6a will be of the required sign when the input is -x, but the sign of y with an input of +x cannot be reconciled with mathematical convention. However, the circuit of Fig. 8.6a does provide a consistant output when the function is x^2 , with inputs of $\pm x$. Much the same applies to the Fig. 8.6b circuit, which shows the function general regression of the fig. 8.6b circuit, which shows the function general contraction of the fig. 8.6b circuit, which shows the function give most outputs for a square function, but not for a cube function, by employing an extra sign reversing amplifier.

Getting away now from the complexities of square roots of negative numbers and other mathematical anomalies, Fig. 8.6d can be made to give outputs of $y = x^2 + x_0$, or some other combination such as $y = x^{12} - 3x$, depending on the choice of function, voltage polarities, and computing resistor values. The purpose of other circuits E-H will be self-evident in Fig. 8.6. Fig. 8.6 gives the symbol sunknown and also be introduced into problem set-ups where integrating amplifiers are used, as its frequency response is well in excess of any frequency likely to be encountered.

Next month: The final item of the PEAC equipment, UNIT "D", will be described.

NEWS BRIEFS

All Change at British Rail

PROGRESS of trains over 47 miles of track in the Leeds area is being monitored and controlled with the aid of a computer recently installed at Leeds City station.

The system is unique in that it uses a standard commercial computer (an Elliott 903) which can easily be adapted to new needs. Older systems using Post Office relays and uniselectors have to be extensively rewired when changes are called for.

The Leeds signalmen never see the trains they control. Each locomotive is given a code number which appears on one of 67 eathode ray tube readouts on a large track diagram, and its progress is traced by coloured lights. When the train reaches a new stretch of track, the computer transfers the code number to a fresh readout.

British Rail say future developments could include automatic signal route setting and automatic train announcing.

Lincompex Goes Afloat

Lincomes, a Post Office invention which improves the intelligibility of high-frequency radio letphone services, for "Linked coorders on three Cunard Liners. Lincompex at the sending end to maintain the caller's voice at a maximum level thus preserving a good signal-do-noise maximum level thus preserving a good signal-do-noise control signal links the two.

Slow Motion Tube

ENCLIBE ELECTRIC VALVE have developed the electronic equivalent of a high-speed oine camera. It consists of an electrostatically-focused triode image-converter tube which can present on its integral fluorescent screen a sequence of frames showing the development of a high-speed event. The shutter action is achieved by deflection of the electron beam over a slit in an aperture plate in the tube. Speeds of 20 million frames per second can be obtained, and, dependent on image size, the number of frames to be recorded can be from eight to 32.

(below) Control panel of the "windowless" signal box at Leeds City station

